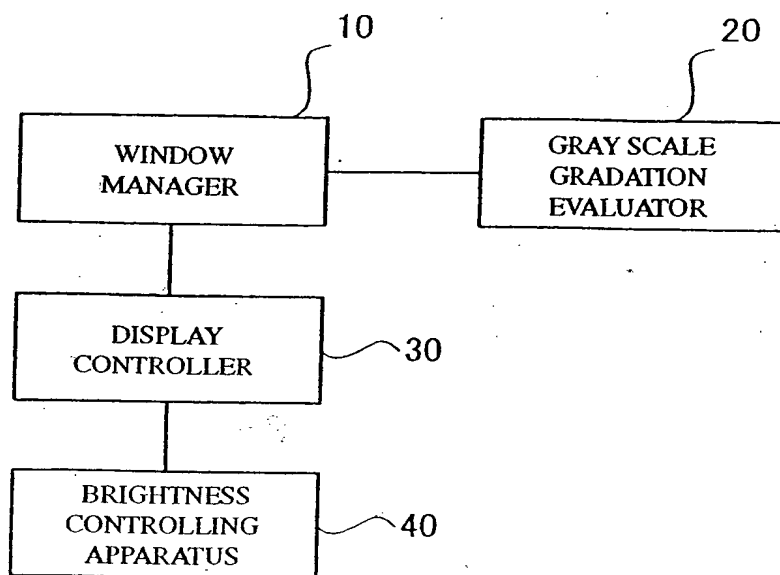


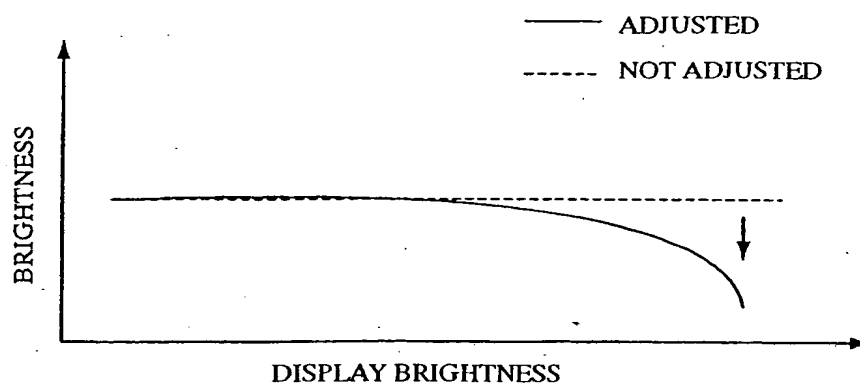
[Figure 1]

(1/9)

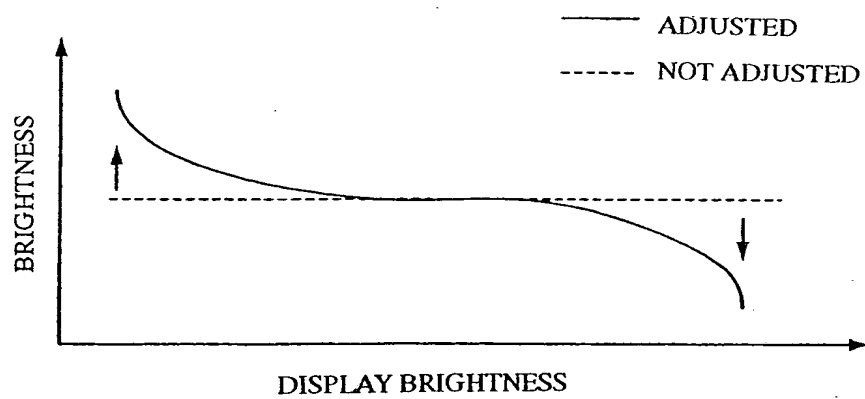


[Figure 2]

(2/9)

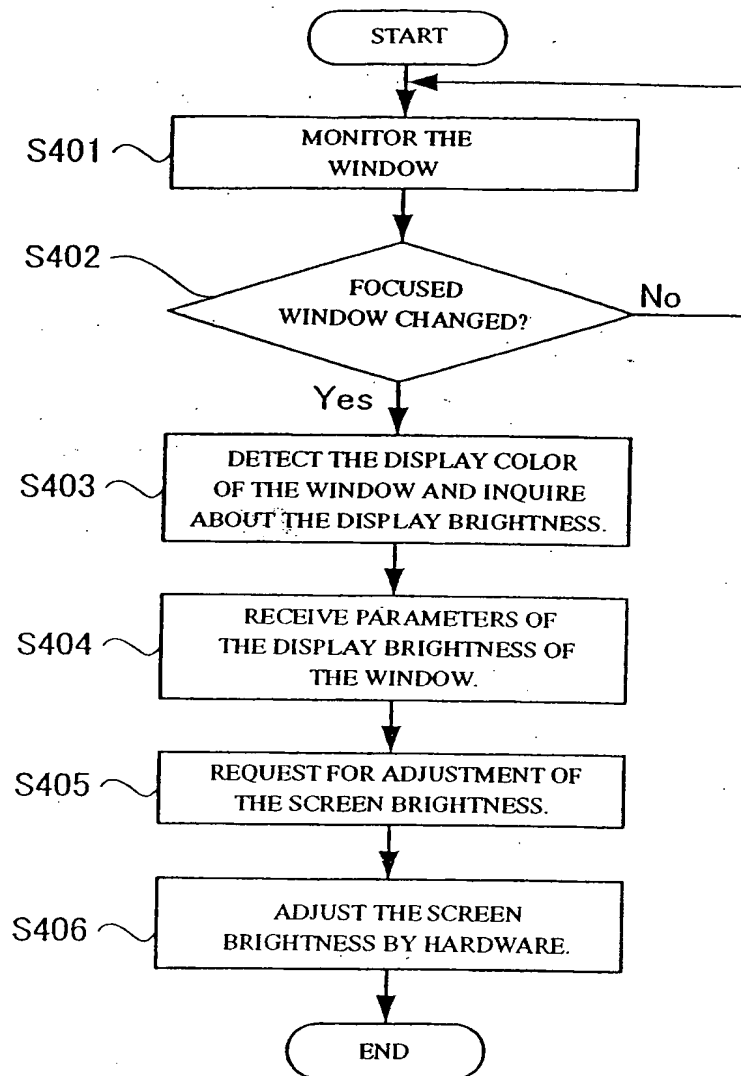


[Figure 3]



[Figure 4]

(3/9)



intel

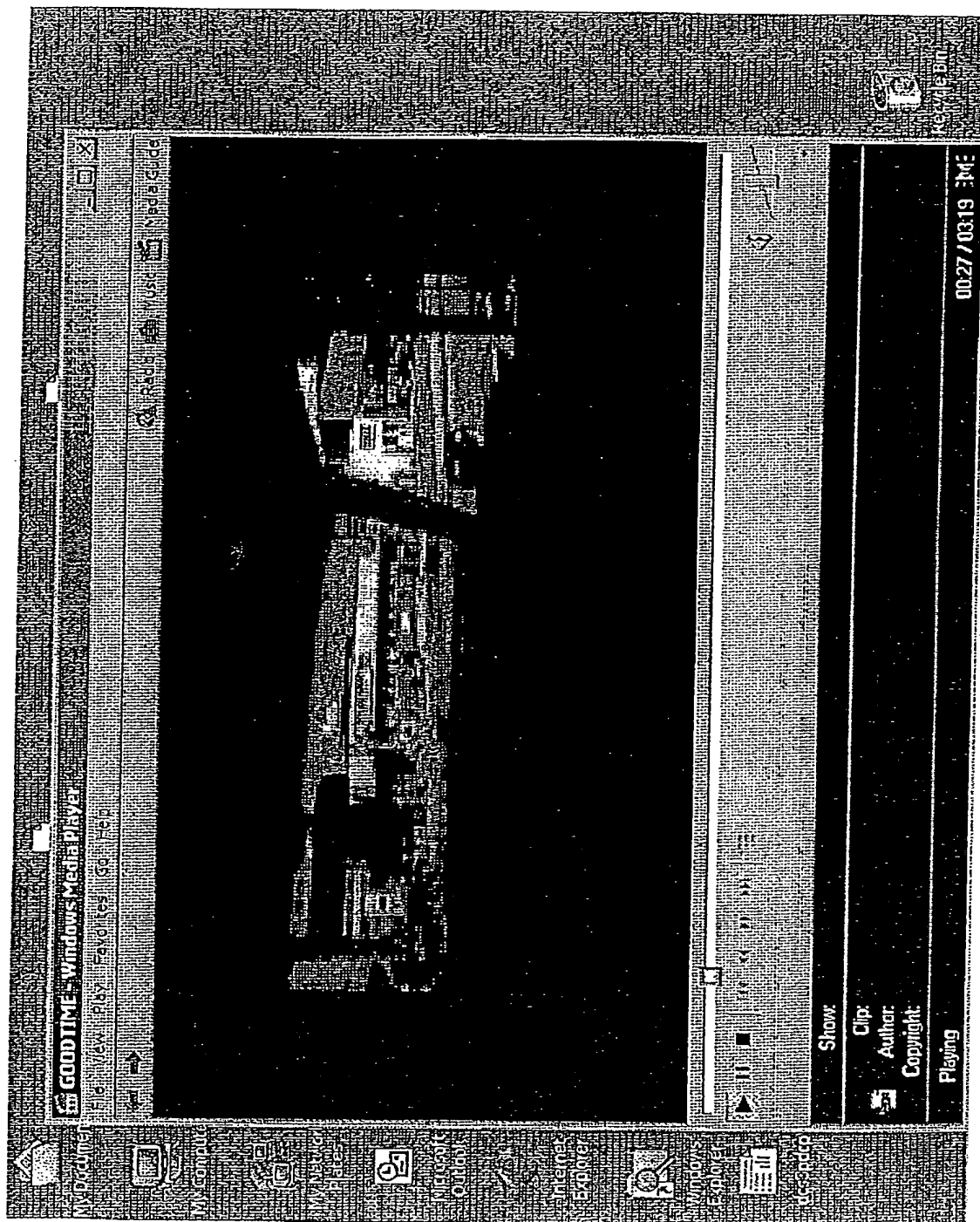
82371AB PCI-TO-ISA / IDE XCELERATOR (PIIX4)

<ul style="list-style-type: none"> □ Supported Kits for both Pentium™ and Pentium™ Microprocessors <ul style="list-style-type: none"> — 82430TX ISA Kit — 82440LX ISA/DP Kit □ Multifunction PCI to ISA Bridge <ul style="list-style-type: none"> — Supports PCI at 30 MHz and 33 MHz — Supports PCI Rev 2.1 Specification — Supports Full ISA or Extended I/O (EIO) Bus — Supports Full Positive Decode or Subtractive Decode of PCI — Supports ISA and EIO at 1/4 of PCI Frequency □ Supports both Mobile and Desktop Deep Green Environments <ul style="list-style-type: none"> — 3.3V Operation with 4V Tolerant Buffers — Ultra-Low Power for Mobile Environments Support — Power-On Suspend, Suspend to RAM, Suspend to Disk, and Soft-OFF System States — All Registers Readable and Restorable for Proper Resume from 0V Suspend □ Power Management Logic <ul style="list-style-type: none"> — Global and Local Device Management — Suspend and Resume Logic — Supports Thermal Alarm — Support for External Microcontroller — Full Support for Advanced Configuration and Power Interface (ACPI) Revision 1.0 Specification and OS Directed Power 	<ul style="list-style-type: none"> — Integrated 16 x 32-bit Buffer for IDE PIO Burst Transfers — Supports Glue-less "Swap-Bay" Option with Full Electrical Isolation □ Enhanced DMA Controller <ul style="list-style-type: none"> — Two 82C37 DMA Controllers — Supports PCI DMA with 3 PCI/PCI Channels and Distributed DMA Protocols (Simultaneously) — Fast Type-F DMA for Reduced PCI Bus Usage □ Interrupt Controller Based on Two 82C59 <ul style="list-style-type: none"> — 15 Interrupt Support — Independently Programmable for Edge/Level Sensitivity — Supports Optional I/O APIC — Serial Interrupt Input □ Timers Based on 82C54 <ul style="list-style-type: none"> — System Timer, Refresh Request, Speaker Tone Output □ USB <ul style="list-style-type: none"> — Two USB 1.0 Ports for Serial Transfers at 12 or 1.5 Mbit/sec — Supports Legacy Keyboard and Mouse Software with USB-based Keyboard and Mouse — Supports UHCI Design Guide □ SMBus <ul style="list-style-type: none"> — Host Interface Allows CPU to Communicate Via SMBus — Slave Interface Allows External SMBus Master to Control Resume Events □ Real-Time Clock
--	--

BEST AVAILABLE COPY

[Figure 6]

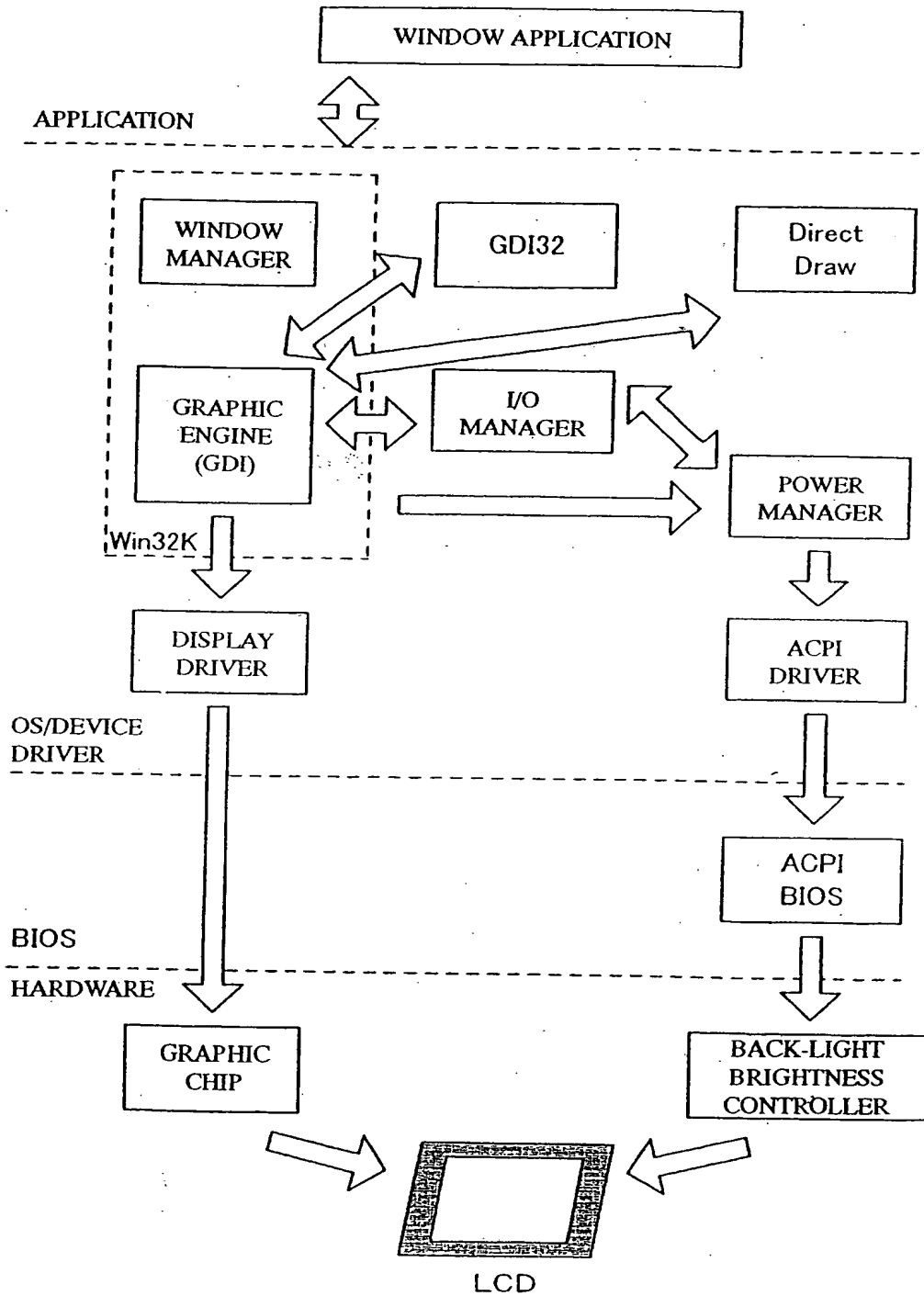
(5/9)



BEST AVAILABLE COPY

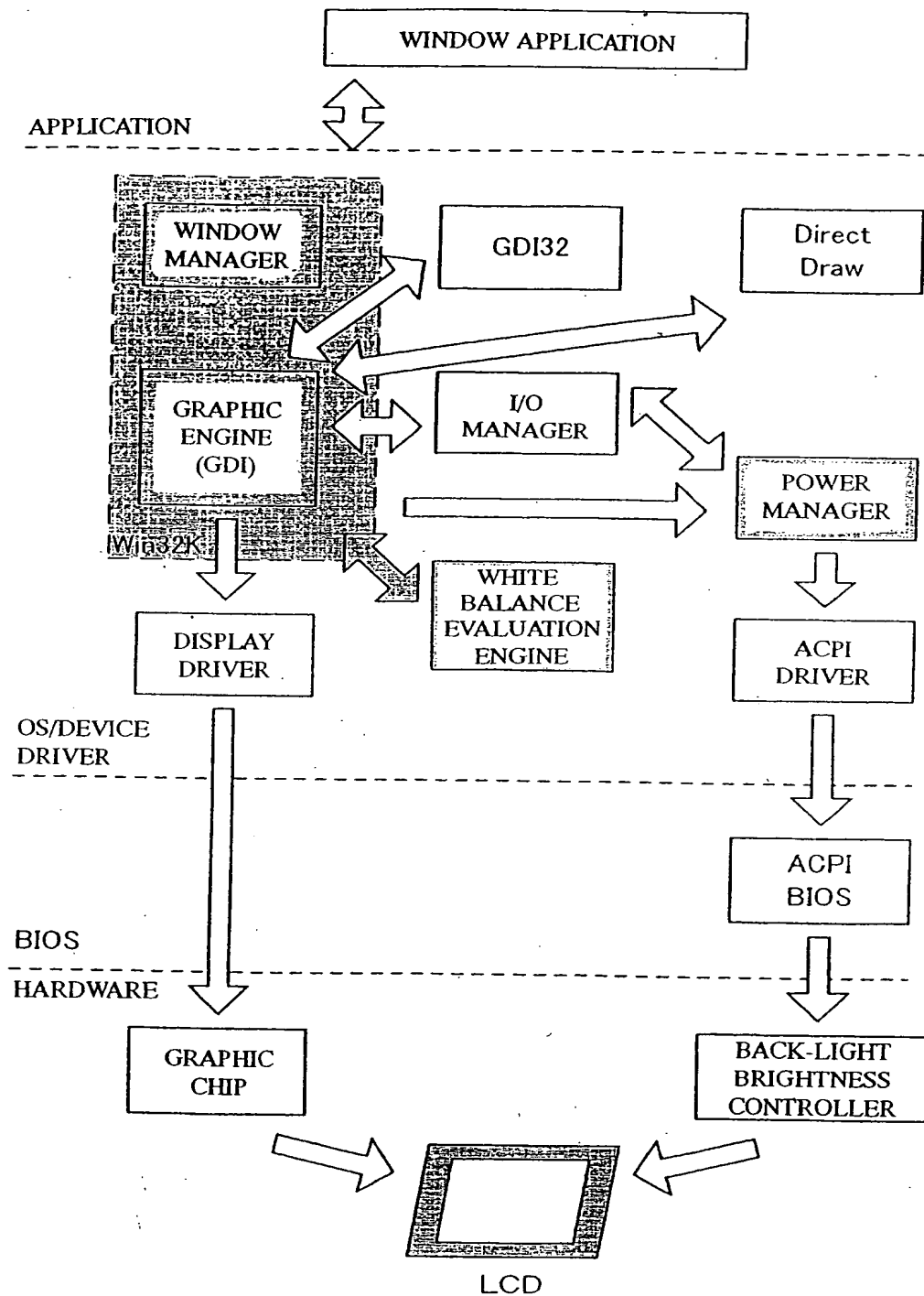
[Figure 7]

(6/9)



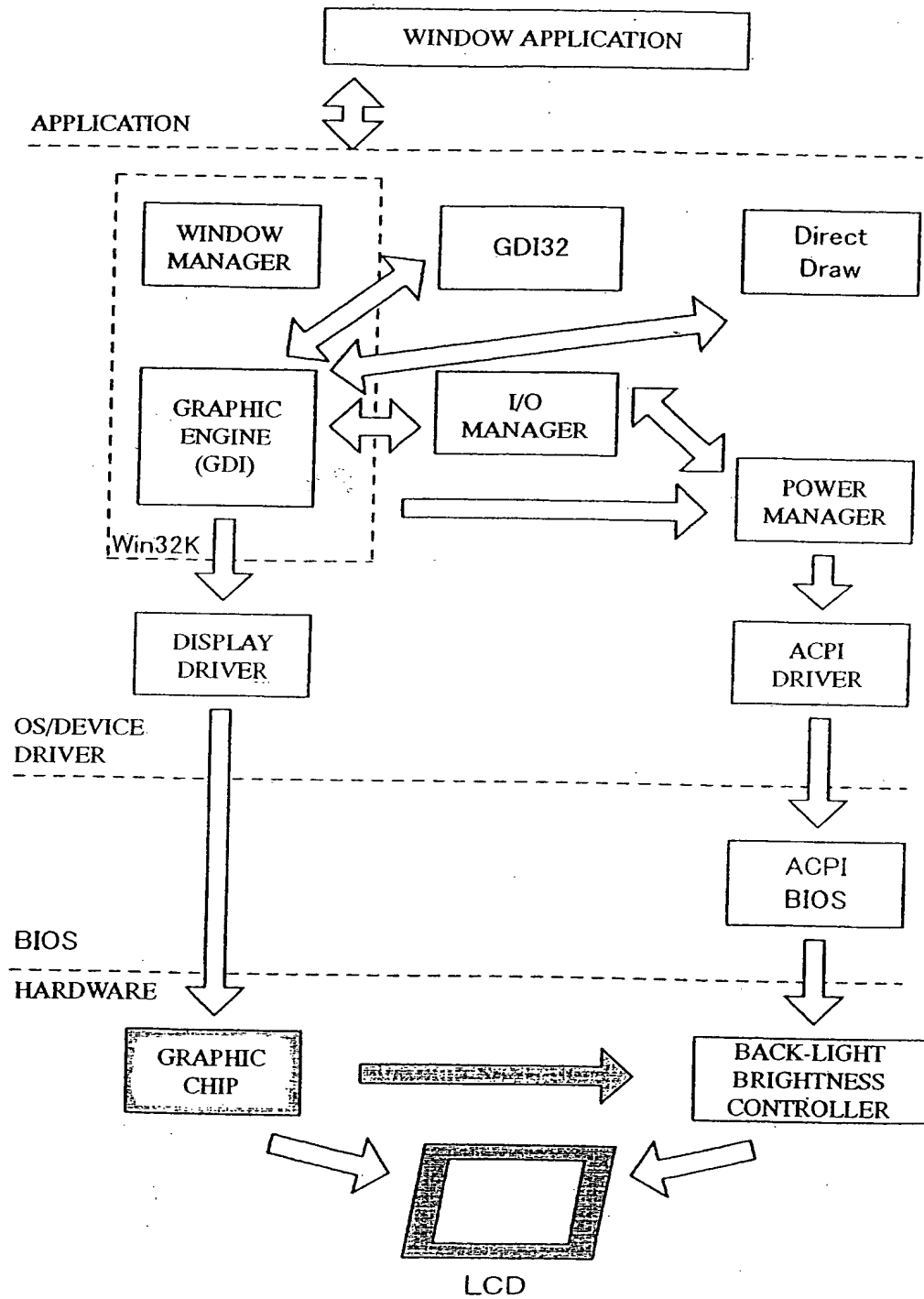
[Figure 8]

(7/9)



[Figure 9]

(8/9)



[Figure 10]

